

Anisotropic Growth of Nonlayered CdS on MoS₂ Monolayer for Functional Vertical Heterostructures

Wei Zheng, Wei Feng, Xin Zhang, Xiaoshuang Chen, Guangbo Liu, Yunfeng Qiu, Tawfique Hasan, Pingheng Tan, and Ping An Hu*

2D semiconductors have emerged as a crucial material for use in nextgeneration optotelectronics. Similar to microelectronic devices, 2D vertical heterostructures will most likely be the elemental components for future nanoscale electronics and optotelectronics. To date, the components of mostly reported 2D van der Waals heterostructures are restricted to layer crystals. In this work, it is demonstrated that nonlayered semiconductors of CdS can be epitaxially grown on to 2D layered MoS₂ substrate to form a new quasi vertical heterostructure with clean interface by chemical vapor deposition. Photodetectors based on this CdS/MoS₂ heterostructure show broader wavelength response and \approx 50-fold improvement in photoresponsivity, compared to the devices fabricated from MoS₂ monolayer only. This research opens up a way to fabricate a variety of functional quasi heterostructures from nonlayered semiconductors.

1. Introduction

Semiconductor heterostructures play a crucial role in modern microelectronics and optoelectronics.^[1] Heterostructures, combining different materials, show bandgap offset and tunable electrical and optoelectrical properties. Conventional heterostructures are primarily based on group IV, III-V, or II-VI semiconductors, with covalent bonding between atoms at the hetero-interface.^[1,2] The usage of material components for

W. Zheng, W. Feng, X. Chen, G. Liu, Dr. Y. Qiu, Prof. P. A. Hu Key Laboratory of Micro-systems and Micro-structures Manufacturing of Ministry of Education Harbin Institute of Technology Harbin 150080, P. R. China E-mail: hupa@hit.edu.cn W. Zheng, W. Feng, Prof. P. A. Hu School of Materials Science and Engineering Harbin Institute of Technology Harbin 150080, P. R. China X. Zhang, Prof. P. H. Tan State Key Laboratory of Superlattices and Microstructures Institute of Semiconductors Chinese Academy of Sciences Beijing 100083, P. R. China Dr. T. Hasan Cambridge Graphene Centre University of Cambridge Cambridge CB3 0FA, UK

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conventional heterostructures is strongly dictated by lattice mismatch which determines the interface quality and thus, the heterostructure performance. Beyond the traditional group IV, III-V, or II-VI semiconductors, 2D lavered crystals (e.g., graphene,^[3] transition metal dichalcogenides,^[4] hexagonal boron nitride (h-BN),^[5] phosphorene,^[6] etc.) have emerged as promising candidates for next generation electronics and optoelectronics due to their unique properties. These 2D layered materials can be artificially combined to fabricate various van der Waals (vdW) heterostructures without the lattice match limitation. Novel physical properties of these vdW heterostructures have been investigated theoretically and

experimentally, and devices based on those new heterostructures such as tunnel transistors and sensors have already been demonstrated. $^{\left[7-11\right] }$

This far, these vdW heterostructures have mainly been fabricated by a top-down process of manual transfer or a bottomup method of chemical vapor deposition (CVD) growth. The first demonstration of vdW heterostructures were realized by vertically stacking different 2D materials (graphene/h-BN,^[8,12] MoS₂/graphene,^[10] graphene/WS₂,^[9] etc.) using conventional polymethyl-methacrylate-mediated transfer method.^[7] The physical properties of these heterostructures are significantly influenced by relative orientation of the layers and interfacial quality between them. However, the stacking style and crystal orientation cannot be easily controlled by mechanical transfer method. In addition, such strategies cannot ensure good interfacial quality. Compared to manual transfer, CVD epitaxial growth is a powerful approach for fabricating 2D vdW heterostructures with controlled stacking style, crystal orientation, and clean interface. Indeed, using this strategy, some vertical heterostructures have already been successfully grown, including graphene/h-BN,^[13] MoS₂/h-BN,^[14] MoSe₂/graphene,^[15] MoS₂/ graphene,^[16] WS₂/MoS₂,^[17] and MoS₂/SnS₂.^[18]

This far, the components of the reported 2D-vertical vdW heterostructures have been restricted to layered materials with planar crystal structures. However, many non-layered materials such as cadmium sulfide (CdS) also exhibit attractive optoelectronic properties.^[19] Combination of such non-layered functional semiconductors with layered materials (e.g., MoS₂) could create a new type of vdW heterostructure to provide novel



Figure 1. Schematic illustration of one-step epitaxial growth of CdS/MoS₂ heterostructures.

platform for applications in nanoscale electronics and optoelectronics. These non-layered materials typically incline to stack into 3D nanostructures due to their chain-like structure and the lack of driving force for 2D anisotropic growth. We note that some non-layered materials such as noble metals, metal oxides, and metal chalcogenides can be confined to 2D anisotropic growth to form sheet-like nanomaterials through the assistance of organic surfactants via wet chemical synthesis.^[20] However, these soluble sheet-like ultrathin nanomaterials cannot be used for device applications as they become easily folded, crumpled, or aggregated when they are transferred onto substrates during device fabrication. In addition, these surface-ligand protected nanosheets are not suitable for the fabrication of clean interfaces. To realize these novel vdW heterostructures with high quality interface, direct anisotropic growth of non-layered semiconductors on layered 2D materials through a CVD process is the most realistic approach. However, such a process is yet to be demonstrated.

In this work, epitaxial growth of non-layered CdS nanosheet on ultrathin MoS₂ is demonstrated, thereby creating a new vdW vertical heterostructure. It is found that anisotropic growth of CdS on MoS₂ is driven by kinetics within a certain reaction temperature range influenced by the substrates, without forming energetically favored 3D structures. Photodetectors based on this CdS/MoS₂ heterostructure show broader wavelength response and significantly improved photoresponsivity compared to the devices fabricated from monolayer MoS₂ only.

2. Results and Discussion

Figure 1 shows the schematic representation of epitaxial growth of 2D CdS/MoS₂ vertical heterostructures (further information in Figure S1, Supporting Information). In the first stage, the left zone and MoO₃ zone in the furnace are heated to 250 and 670 °C, respectively. Subsequently, sulfur is pushed into the left zone quickly and kept for 25-30 min. This stage is used to grow MoS₂. In the second stage, the zone of substrate containing asgrown MoS₂ cools down to 500–600 °C, meanwhile CdS zone is heated to 950 °C. Finally the CdS stream is submitted using Ar gas to the surface of MoS₂ to form the vertical heterostructure.

The as-grown triangular MoS2 nanoflakes with sizes of up to 30 µm lateral dimension are uniformly distributed over the substrate. Such a typical MoS₂ flake is shown in Figure 2a. Figure 2b shows a scanning electron microscope (SEM) image of the vertical

CdS has two different crystal structures,^[21] it is necessary to determine their crystallographic phase using X-ray diffraction (XRD). In Figure 2e, the dark gray plot represents the



Figure 2. Characterization of vdW epitaxial CdS/MoS₂. a,b) SEM images of CVD grown triangle MoS₂ and CdS/MoS₂ heterostructures, respectively. c) AFM image and associated height profile of a hexagonal CdS. d) AFM phase image of the heterostructure with associated height profiles. e) XRD characterization of from top to bottom MoS₂/CdS, MoS₂, and PDF#41-1049.

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Figure 3. TEM characterization of the heterostructures. a) Low-magnification TEM images of CdS/MoS₂ heterostructures. b) Selected area electron diffraction (SAED) of heterostructures. c,d) Fast Fourier transform images of CdS and MoS₂, respectively. e) Cross-sectional TEM images of the CdS/MoS₂ heterostructure. f) Schematic model of the CdS/MoS₂ heterostructures.

XRD result of MoS₂, the light gray plot shows the CdS powder XRD patterns from the database (PDF#41-1049), while the black plot is that of the CdS/MoS₂ heterostructure. The diffraction pattern of the heterostructure can be perfectly indexed on the datebase of a hexagonally phased CdS and MoS₂.

To explore the effects of MoS_2 on the growth of CdS, the CdS/MoS₂ heterostructure is studied by transmission electron microscopy (TEM) (shown in **Figure 3a** and Figure S3, Supporting Information). Under TEM observation, the CdS sheets exhibit a deeper color while the MoS₂ films are almost transparent. A typical selected area electron diffraction (SAED) pattern (Figure 3b) is taken from the area of CdS/MoS₂ heterostructure (circle in Figure 3a). In this SAED pattern, two sets of six-fold symmetric diffraction spots are observed, in which the brighter and inner spots correspond to CdS (a = 4.14 Å) while the weaker set belongs to MoS₂ (a = 3.14 Å). By correlating the SAED pattern with the morphology (Figure 3a), indicating that every plane of CdS is parallel to the corresponding MoS₂ plane, e.g., CdS (100) || MoS₂ (100)

and CdS (110) || MoS₂ (110). Figure 3c,d shows the Fourier transform high-resolution TEM of CdS and MoS₂ which were measured from the red and blue squares in Figure 3a, respectively. Both CdS and MoS2 exhibit high quality crystallinity. The results indicate that the spacings of (100) and (110) planes of CdS are 0.36 and 0.21 nm, while the planes of MoS₂ are 0.27 and 0.16 nm, respectively. These TEM characterization further demonstrate the good epitaxy of CdS on MoS₂ without any misorientation. Lattice mismatch between hexagonal CdS and MoS₂ is calculated to be 32% using the relationship $f = [d_{Cds}(100) - d_{MoS2}(100)]/d_{MoS2}(100),^{[22]}$ where $d_{\rm Cds}(100) = 0.359$ nm, $d_{\rm MoS2}(100) = 0.272$ nm. This lattice mismatch is larger compared to the traditional group IV, III-V, or II-VI semiconductor heterostructures.^[23] vdW epitaxy allows formation of high quality heterostructure consisting of two crystals with such large lattice mismatch because of the weak vdW interaction at the interface. We note that this is also applicable to manually stacked crystals. The cross-sectional electron microscope images with clean interfaces without significant interlayer atomic mixing is shown in Figure 3e, guaranteeing the heteroepitaxial stacking. In addition, as shown in the SEM image in Figure S4 (Supporting Information), the CdS nanosheets show parallel edges, as indicated by the blue and red lines. This crystal orientation alignment (CdS (100) || MoS₂ (100) and CdS (110) || MoS₂ (110)) is attributed to vdW epitaxy. Based on the above discussion, we present a scheme of epitaxially grown CdS on MoS₂ in Figure 3f. This shows that hexagonal CdS is grown parrallel to the (001) plane surface of MoS₂ in the six equivalent directions (such as 110), rather than the preferential (001) direction. It is proposed that this growth kinetics is induced by the MoS₂ substrate.

To further investigate our proposal on the anisotropic growth of CdS by substrate induced kinetics, a controlled experiment is performed on the growth of CdS nanocrystals on SiO₂/Si under identical reaction parameters. This produces rod-like CdS nanocrystals, clearly showing an energetically favorable growth direction of (001) the hexagonal crystals (Figure S5, Supporting Information). This substrate-dependent growth can be well-understood by its surface energy $E_{\rm s}$ or its attachment energy E_a . A smaller value of E_s ensures higher stability while a larger $|E_a|$ indicates a faster growth process. The value of E_s and $|E_a|$ of (001)_{CdS}, calculated by ref. [21] are much higher than any other CdS plane. This indicates that the surface of (001)_{CdS} is more active, with much faster growth rate than any other face under normal condition. This is why compared to the growth of CdS on MoS₂ (Figure 2b), CdS tripod nanocrystal is predominantly grown on SiO₂/Si (Figure S5a, Supporting Information). Indeed, the growth rate of the legs along the (001) crystallographic orientation on SiO2/Si is much higher than that along the other surfaces (the model of tripod nanocrystal is shown in Figure S6, Supporting Information). On the other hand, the shapes grown on MoS₂ are different due to the substrate lattice structure and the energy barrier for nucleation (ΔG^*) at the growth temperature. Reference [24] has recently discussed growth kinetics on layered crystals that explains our observation. According to classical nucleation kinetics, the free energy change for nucleation at heterostructures can be written as follows: $\Delta G_{r,\gamma} = \pi r^2 t \Delta G_v + \pi r^2 (\gamma_c + \gamma_{sc} - \gamma_s) +$ $2\pi r t \gamma_{c,edge}$.^[24] Here r and t are radius and thickness of nucleus



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of CdS. $\gamma_{\rm s}$, $\gamma_{\rm c}$, $\gamma_{\rm sc}$, and $\gamma_{\rm c,edge}$ represent the $E_{\rm s}$ of the substrate (MoS₂ or SiO₂ in our case), E_s of the nucleus of the CdS crystals, interfacial energy between the nucleus and the substrate and the E_s of nucleus edge of CdS crystals, respectively. The difference in free energy per unit volume during nucleation is expressed as ΔG_{v} . The maximum point $\Delta G_{r,\gamma}$ in the total Gibbs free energy corresponds to the critical free energy barrier for nucleation $\Delta G^* = \pi r (\gamma_{c,edge})^2 / [\Delta G_v - (\gamma_c + \gamma_{sc} - \gamma_s)/t].^{[24]}$ The term $(\gamma_c + \gamma_{sc} - \gamma_s)$ represents the effective change in E_s during the nucleation and is largely dependent on the growth substrate. The surface energy of MoS₂ is \approx 70–75 mJ m^{$-\overline{2}$}.^[25] On the other hand, CdS grown on SiO₂ forms tripod structures due to the significantly larger $E_{\rm s}$ of the substrate ($\gamma_{\rm SiO2} \approx 300-400$ mJ m⁻²).^[26] This indicates that a larger free energy barrier must be overcome for CdS growth on SiO2/Si compared to the epitaxial growth of CdS on MoS₂ surface. It is also noted that the CdS growth is further impacted by the reaction temperature. Above 1000 °C, CdS nanocrystals always grow in energically favorable (001) direction on both the SiO₂ and MoS₂ substrates, forming rod-like nanocrystals (Figure S5b, Supporting Information). This is mainly attributed to the high concentration of precursor and the enhanced activity of CdS is at such high temperature (>1000 °C). Since the growth rate increases faster along the (001) plane compared to that of the (100) plane under high temperature, the CdS nanocrystals tend to develop a rod-like morphology. Therefore, tripod-like structures (Figure S5b, Supporting Information) rather than nanosheets (Figure 2b) of CdS nanocrystals are observed on MoS₂ surface for high temperature growth.

Raman and photoluminescence (PL) are effective ways to characterize the crystal quality and band gap of these materials. The CdS/MoS₂ heterostructures (hexagonal CdS on MoS₂ in Figure 4a and triangular CdS on MoS₂ in Figure S7, Supporting Information) are characterized by Raman and PL with a 488 nm



Figure 4. Spectroscopic characterizations of the as-grown CdS/MoS₂ heterostructures. a) SEM image of hexagonal CdS on MoS₂. b) Raman mapping of CdS at 300 cm⁻¹. c) Raman mapping of MoS₂ at 402.1 cm⁻¹. d,e) Raman and PL spectra of the CdS/MoS₂ heterostructures. f) UV-vis–NIR spectra of the CdS/MoS₂ heterostructures and single-layer MoS₂. g) Band gap schematic of CdS/MoS₂ heterostructure photodetector under illumination.





laser. The Raman mapping of CdS 1LO mode at 300 cm⁻¹ is shown in Figure 4b. The blue hexagon in the center is the CdS hexagon while the red zone represents MoS₂. Figure 4c shows the Raman mapping at A_{1g} mode of MoS₂. Raman signal of MoS₂ can be obtained from the entire area of the sample. However, the Raman signal of the central area where CdS is grown is much weaker. This Raman mapping further demonstrates the epitaxial nature of the structure. The Raman spectra of the heterostructures are shown in Figure 4d. The peaks of E_{2g} mode at 383 cm⁻¹ and A_{1g} mode at 402 cm⁻¹ are the characteristic peaks of MoS₂. On the other hand, the peaks at 212, 254, 300, and 601 cm⁻¹ are the characteristic peaks of CdS.^[19] The PL spectra of the heterostructures are shown in Figure 4e. We identified PL peak of MoS₂ at 1.81 eV. However, this peak disappeared after the formation of CdS/MoS₂ heterostructure, accompanied by the appearance of a new peak at ≈1.70 eV (Details on PL measurements using a 532 nm laser is presented in Figure S8, Supporting Information). The peak at 1.70 eV can be attributed to the interlayer excitonic transition of MoS₂ conduction band maxima to the CdS valence band minima.^[17] The three peaks in red line at 2.477, 2.491, and 2.493 eV indicate the Raman peaks of Si, A_{1g} mode and E_{2g} mode of MoS₂, respectively. The peak at 2.45 eV indicates the existence of CdS. Because CdS has a strong visible optical absorption, it is an attractive material for enhancement of light absorption and photoresponse of the heterostructure. UV-vis-NIR absorption spectra of MoS₂ and CdS/MoS₂ heterostructures are measured by transferring 0.5 cm \times 0.5 cm MoS₂ and CdS/MoS₂ films on to glass substrates; Figure 4f. The coverage of CdS on MoS₂ is $\approx 20\%$,

estimated from a series of SEM images. The plots indicate that the single layer MoS₂ has a weak light absorption and a small response scope (wavelength < 680 nm). Compared to MoS₂, the CdS/MoS₂ heterostructures have a much higher absorption and wider light response (wavelength < 1000 nm). The higher absorption and stronger response can be explained by the band structure of the heterostructure; Figure 4g.^[27] Due to lower conduction band edge of MoS2, electrons located in the conduction band of CdS tend to transfer to the conduction band of MoS₂, which leads to effective separation of electron-hole pairs and prevents the electron-hole recombination. Therefore, the number of carriers increases in the circuit, leading to the enhancement of photocurrent. It is noted that the broadening of response wavelength of the heterostructure is expected to reach to up to 730 nm after the formation of the vertical heterostructure. However, the UV-vis-NIR spectra of the CdS/MoS2 heterostructures indicates absorbance above 730 nm. This is likely due to the variation in shape, size and thickness of the CdS nanoscrytals.

The single-layer MoS_2 and CdS/MoS_2 heterostructurebased photodetectors are fabricated using Ti/Au contacts on silicon substrates covered with 300 nm silicon oxide (SiO₂/Si). **Figure 5**a presents a schematic diagram of a single-layer MoS_2 photodetector with epitaxially grown CdS. The inset shows an optical microscope image of the actual device. The photoelectrical characterizations are measured at room temperature in ambient condition. As shown in Figure 5b, the photocurrent as a function of time is measured under alternative dark and illumination condition at 0.266 mW cm⁻², with a bias



Figure 5. Characterizations of the photodetectors. a) Schematic of CdS/MoS₂ heterostructure photodetector; inset shows an optical microscope image of the device. b) Time-dependent photoresponse of CdS/MoS₂ heterostructure and single-layer MoS₂ photodetectors under 610 nm illumination, with a 1 V bias. c,d) Photocurrent and photoresponsivity under 365, 490, 550, 610, and 700 nm wavelength of the same devices.



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voltage $V_{ds} = 1$ V and a gate voltage $V_g = 0$ V. At 610 nm (other wavelengths are shown in the Supporting Information) ON and OFF illumination, both the single-layer MoS₂ and CdS/ MoS₂ heterostructure-based photodetectors exhibit repeatable and stable response to the light. The response (light ON) and recovery (light OFF) time is calculated by averaging the device response and considering the time it takes to reach 80% of the final values. The measured rise time is 100 ms, much faster than that of the devices based on CVD grown single-layer MoS₂ (rise time > 10 s). Compared to the MoS_2 photodetector, the CdS/MoS₂ devices exhibit strong photocurrent enhancement under this illumination. Here, photocurrent (ΔI) is defined as the difference between I_{ON} and I_{OFF} with a voltage bias of 1 V. Figure 5c shows the photocurrents of the MoS₂ and CdS/ MoS₂ photodetectors under light with a series of wavelengths (365, 490, 550, 610, and 700 nm). The photocurrent from MoS₂based device gradually drops with the increase in wavelength, while that of the CdS/MoS₂-based device rises fast and reaches the maximum at 700 nm. Note that there is no significant response to light when the illumination wavelength is longer than the optical band gap of the single layer MoS₂ (1.82 eV, $\lambda = 681$ nm). This is because only the incident photons with energies exceeding 1.82 eV excite electrons from the valence band into the conduction band of MoS₂. CdS has a band gap of 2.45 eV. Thus the CdS photodetector is not expected respond to >506 nm. However, we observe photoresponse of the CdS/ MoS_2 detector at >680 nm. We propose that the photoresponse in this forbidden optical absorption region for pristine MoS₂ arises from the photoinduced electron transfer from the valence band of CdS to the bottom of conduction band of MoS₂.

Responsivity (R_{λ}) is another critical parameter to evaluate the performance of a photodetector. R_{λ} is defined as the photocurrent generated per unit power of the incident light on the effective device area. The value of R_{λ} can be calculated using the relation: $R_{\lambda} = \Delta I/P_{\lambda}S$. Here, ΔI is the generated photocurrent, P_{λ} is the incident light intensity, S is the effective illuminated area (In the present study $S \approx 1 \text{ mm} \times 20 \text{ µm}$). The photoresponsivity under different wavelengths of MoS₂ and CdS/ MoS₂ photodetectors is measured and the results are shown in Figure 5d. The results demonstrate that the epitaxial CdS plays an important role to enhance the photoresponsivity, with over 50 times increase in responsivity (70.8 mA W⁻¹ vs 3.91 A W⁻¹) under 610 nm illumination. Such a strong enhancement in photoresponsivity and simple device configuration underscores the potential application of this CdS/MoS₂ heterostructure.

3. Conclusions

In summary, epitaxial growth of nonlayered CdS onto MoS_2 was achieved using a one-step CVD method, resulting in vertical heterostructures. It is proposed that the substrate surface energy plays an important role in the growth of CdS. Furthermore, the feasability of CdS/MoS₂ heterostructure-based photodetectors is demonstrated. The photocurrent and photoresponsivity of CdS/MoS₂ heterostructure-based photodetectors are greatly enhanced, compared to their MoS₂ counterparts, making the presented strategy very attractive for high-performance optelectronic devices.

4. Experimental Section

Characterizations: The as-grown CdS/MoS₂ heterostructures were characterized by SEM (Hitachi S-4200), AFM (Nanoscope IIIa Vecco), UV–vis–NIR absorption (Hitachi U-4100), TEM (Tacnai-G2 F30, accelerating voltage of 300 kV), and Raman spectroscopy (LabRAM XploRA, power of 0.15 mW, excitation wavelength of 488 and 532 nm). The detectors were annealed at 200 °C for 30 min with 100 sccm Ar : H₂ (V/V = 9/1) to reduce the resistance and improve the contact for the devices.

Electrical Measurements: These devices were characterized using a semiconductor analyzer (Keithley 4200 SCS) combined with a Lakeshore probe station. Photoelectric data were obtained by using a 500 W xenon lamp as the light source. Monochromatic lights of 254–850 nm were obtained using optical filters. The intensities of the incident light source were measured by a power and energy meter (Model 372, Scienteck). The photocurrent measurements were performed using the Lakeshore probe station and an HP 4140B Semiconductor Parameter Analyzer.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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